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The diagram shows a 5V voltage regulator circuit. The input is labeled "Unreg". A 100K resistor is connected between the input and the non-inverting input of the op-amp. A 10k resistor is connected between the non-inverting input and the inverting input. A 220 resistor is connected between the inverting input and ground. A 10K resistor is connected between the output and the inverting input. A 1k2 resistor is connected between the output and ground. A capacitor C1 is connected between the output and ground. A Zener diode Z1 (5V6, 100mW) is connected between the output and ground. The output is labeled "+5V reg, switched to PIC power". Three switches (SW1, SW2, SW3) are connected between the output and ground. SW1 is labeled "SW1: high", SW2 is labeled "SW2: high", and SW3 is labeled "SW3: high". A label "HOLD ON: LOW IDLE: O.Drain (OPTION)" is present.

THIS CIRCUIT REPRESENTS A CONCEPT AND SHOULD REQUIRE SOME WORK TO MAKE IT USABLE. USE AT YOUR OWN RISK.